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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Group Art Unit: 2814  
Examiner: Anh D. Mai

In Re PATENT APPLICATION Of:

Applicant : Hiroki NAKAMURA  
Serial No. : 09/625,178  
Filed : July 25, 2000  
For : SEMICONDUCTOR DEVICE HAVING  
WIRING PATTERNS AND DUMMY  
PATTERNS COVERED WITH  
INSULATING LAYER

Attorney Ref. : F98ED0762

AMENDMENT

RECEIVED  
MAR -4 2003  
TECHNOLOGY CENTER 2800

March 3, 2003

**BOX: Non-Fee Amendment**  
Commissioner for Patents and Trademarks  
Washington, D.C. 20231

Sir:

In response to the Examiner's Office Action mailed December 3, 2002,

please amend the above-identified application as follows: